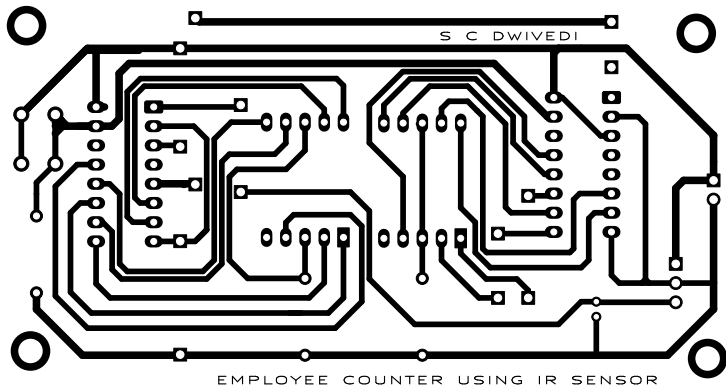




*Fig. 3: Actual-size PCB*